

FIG. 1

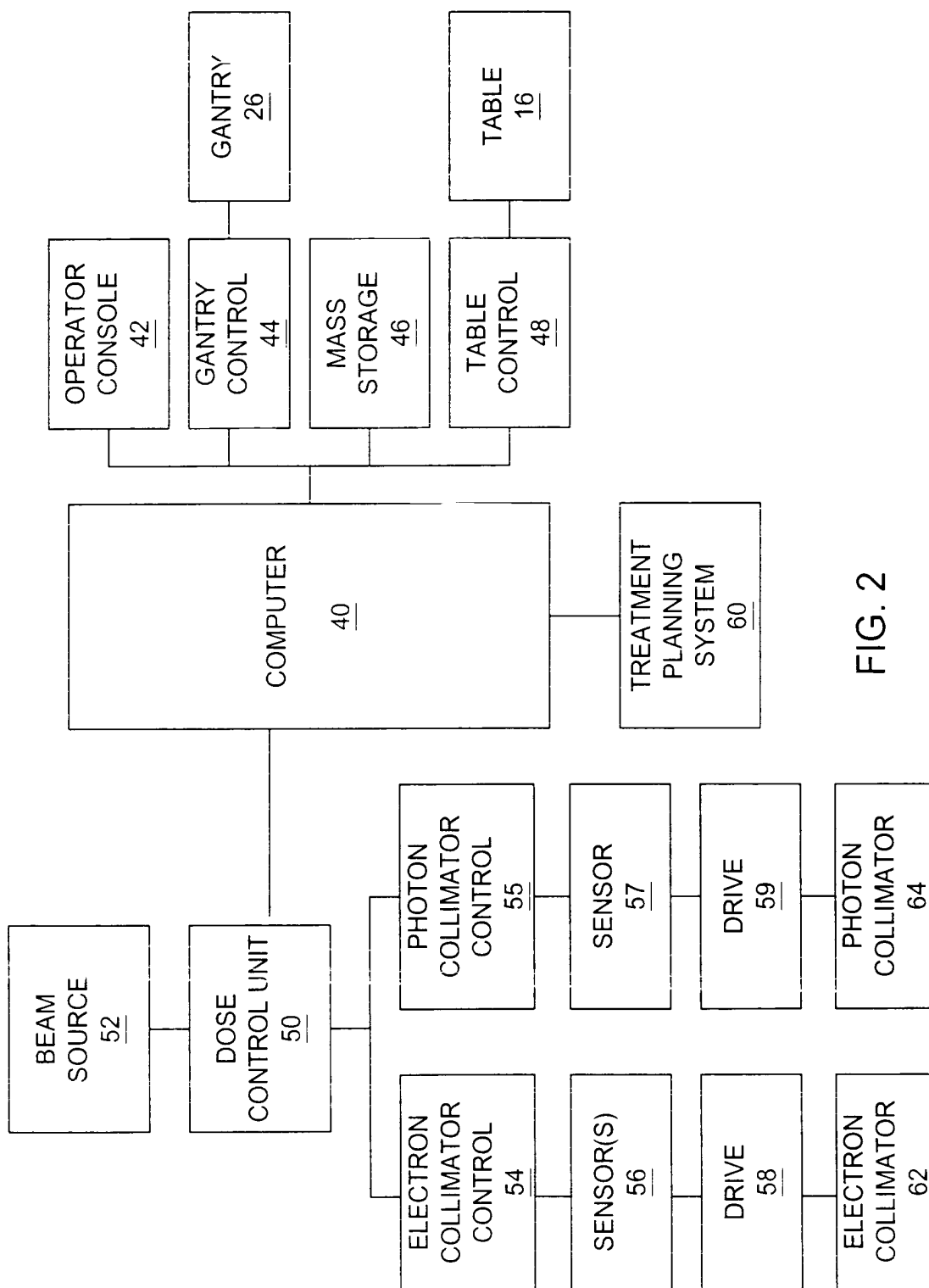


FIG. 2

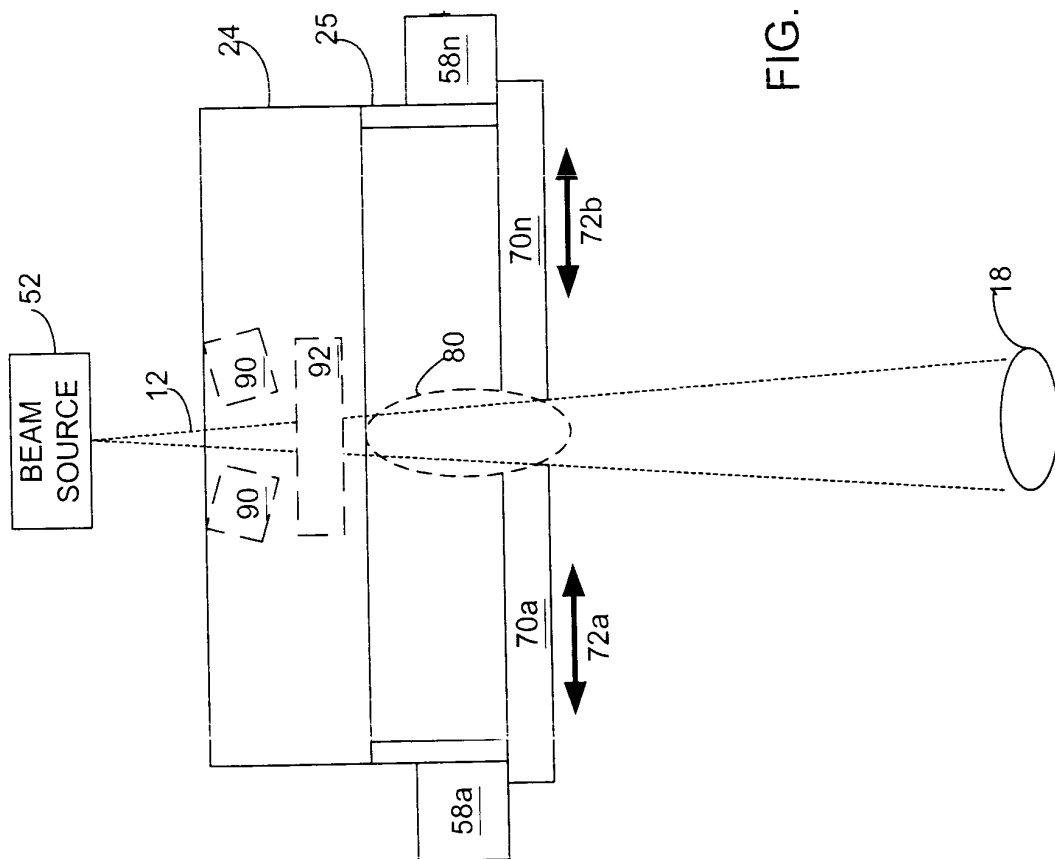


FIG. 3

FIG. 4 is a schematic diagram of a device 62.

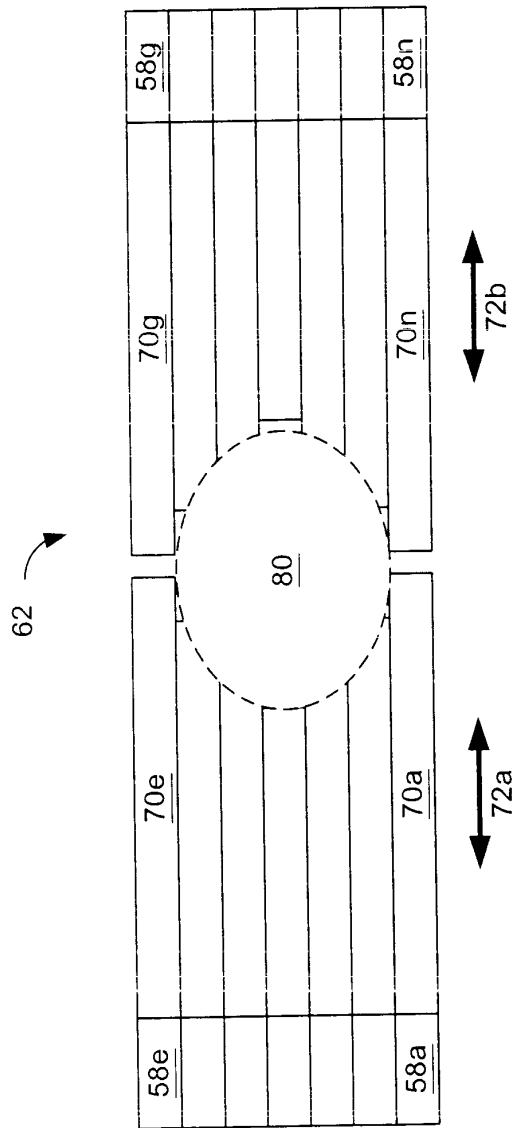


FIG. 4